# END USER LICENCE AGREEMENT FOR THE CORTEX MICROCONTROLLER SOFTWARE INTERFACE STANDARD (CMSIS) DELIVERABLES

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“CMSIS-DAP Specification” means any documentation defining the application programming interface, naming and coding conventions of the Cortex Microcontroller Software Interface Standard Debug Access Port (“CMSIS-DAP”). Notwithstanding the foregoing, “CMSIS-DAP Specification” shall not include: (i) the implementation of other published specifications referenced in the CMSIS-DAP Specification; and (ii) any enabling technologies that may be necessary to make or use any product or portions thereof that complies with the CMSIS-DAP Specification, but are not themselves expressly set forth in the CMSIS-DAP Specification (e.g. compiler front ends, code generators, back ends, libraries or other compiler, assembler or linker technologies; validation or debug software or hardware; applications, operating system or driver software; RISC architecture; and processor microarchitecture).

“CMSIS-DAP Firmware” means the C programming language source code accompanying this Licence which implements the functionality of the application programming interface as defined in the CMSIS-DAP Specification and any updates, patches and modifications ARM may make available under the terms of this Licence.

“CMSIS Deliverables” means the following components: (i) CMSIS-CORE; (ii) CMSIS-DRIVER; (iii) CMSIS-DSP; (iv) CMSIS-PACK; (v) CMSIS-RTOS API; and (vi) CMSIS-SVD .

“CMSIS-CORE” means the specification defining the application programming interface, naming and coding conventions for the Cortex-M processor cores.

”CMSIS-DRIVER” means the specification defining a generic peripheral driver application programming interface, naming and coding conventions.

“CMSIS-DSP” means the digital signal process (DSP) library specification defining the application programming interface of a DSP library implementation.

“CMSIS-PACK” means the specification defining a software pack file format, verification utility, and the associated XML schema file.

“CMSIS-RTOS API” means the real-time operating system (RTOS) specification defining a generic application programming interface layer for a RTOS system.

“CMSIS-SVD” means the specification defining the System View Description (SVD), verification utility, and associated XML schema files.

“Firmware” means firmware that complies with the CMSIS-DAP Specification.

“RDDI DLL” means the reference implementation of a device driver accompanying this Licence in object code form and any updates, patches and modifications ARM may agree to make available under the terms of this Licence and is used with targets containing microprocessors manufactured or simulated under licence from ARM.

“Separate Files” means the components identified in the Schedule..

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SCHEDULE

Separate Files

The package also includes the components contained in the following directories:

(a) ./CMSIS/DSP_Lib - DSP Library sources and examples;
(b) ./CMSIS/Include - Header files;
(c) ./CMSIS/Lib - DSP Library build for various toolchains;
(d) ./CMSIS/RTOS - Header file template for CMSIS-RTOS implementation; and
(e) ./Device - Template files and implementations for Cortex-M class processors.

All of the above components (a– e) are licensed to you under the terms of the BSD licence, which is incorporated within or alongside the above components.

(f) ./CMSIS/Driver – CMSIS-Driver header files
(g) ./CMSIS/Pack – Example Device Family Pack

The above components (f – g) are licensed to you under the terms of the zlib licence, which is incorporated within or alongside the above components.

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